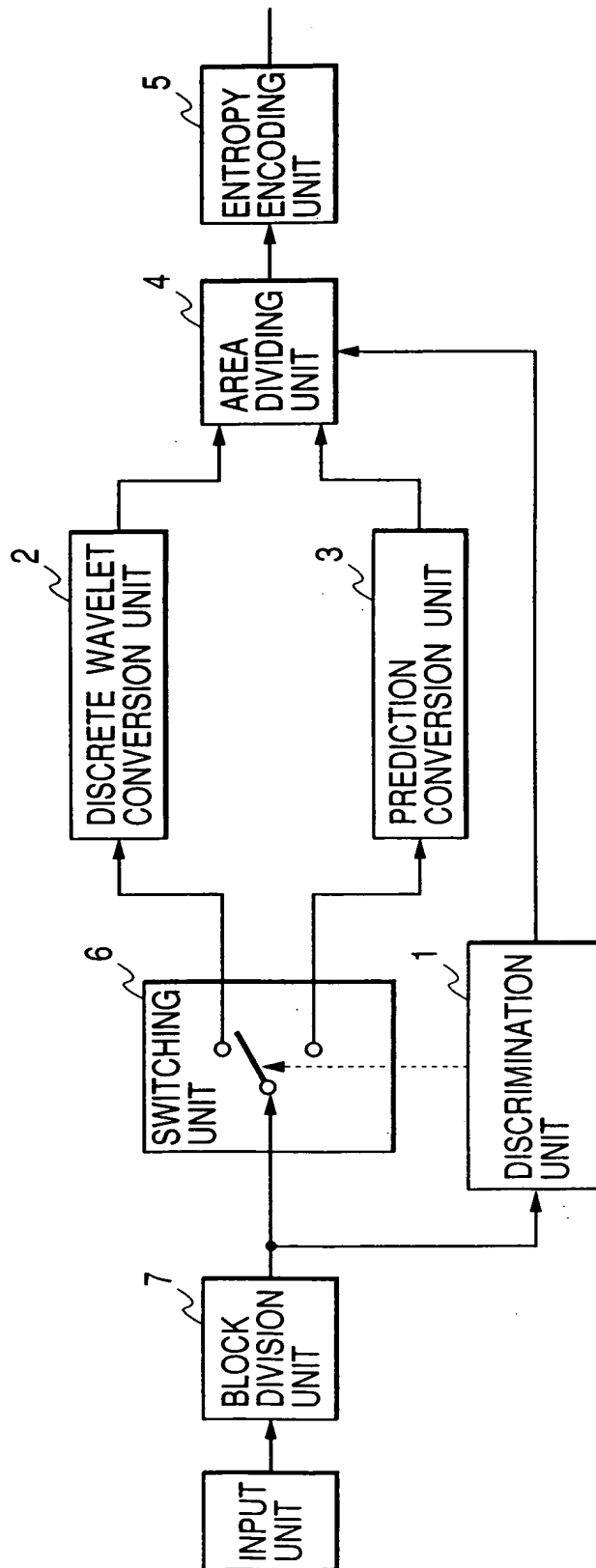
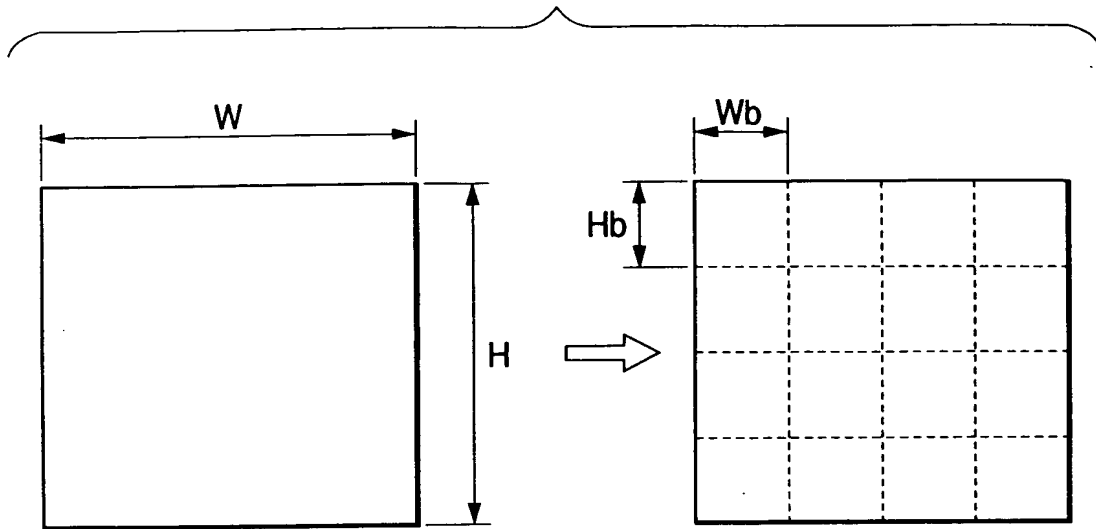
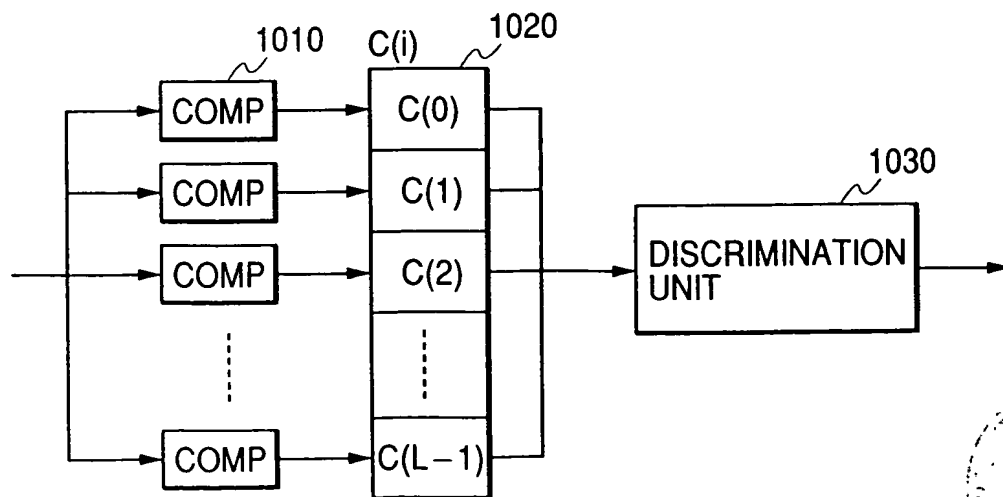


FIG. 1



**FIG. 2****FIG. 3**

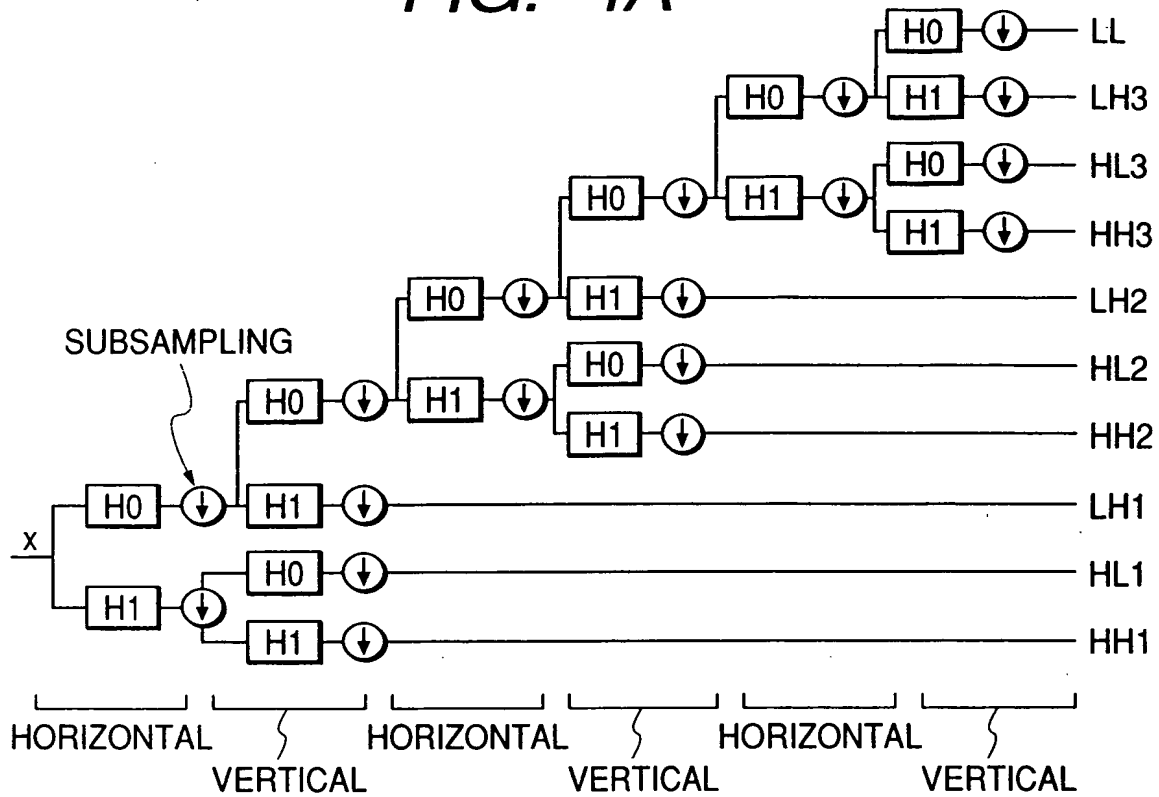
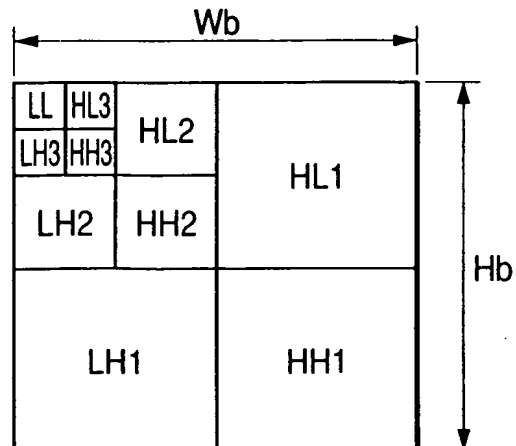
**FIG. 4A****FIG. 4B**

FIG. 5A

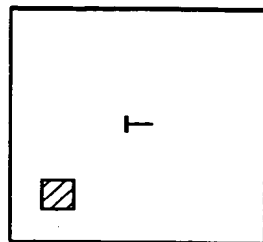


FIG. 5B

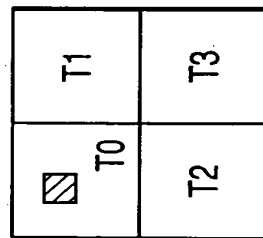


FIG. 5C

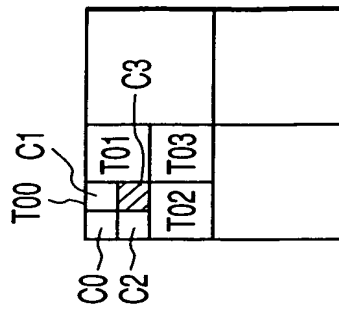


FIG. 5D

| DIVIDED AREAS   | BIT DATA STRING TO BE OUTPUT               |
|-----------------|--|
| T               | 1  |
| T0 T1 T2 T3     | 1 000 000 000<br>T1 T2 T3<br>(3)           |
| T00 T01 T02 T03 | 1 000 000 000<br>T01 T02 T03<br>(2)        |
|                 | 000 0 000 0 000 0 000 0<br>C0 C1 C2 C3 (1) |

FIG. 5E

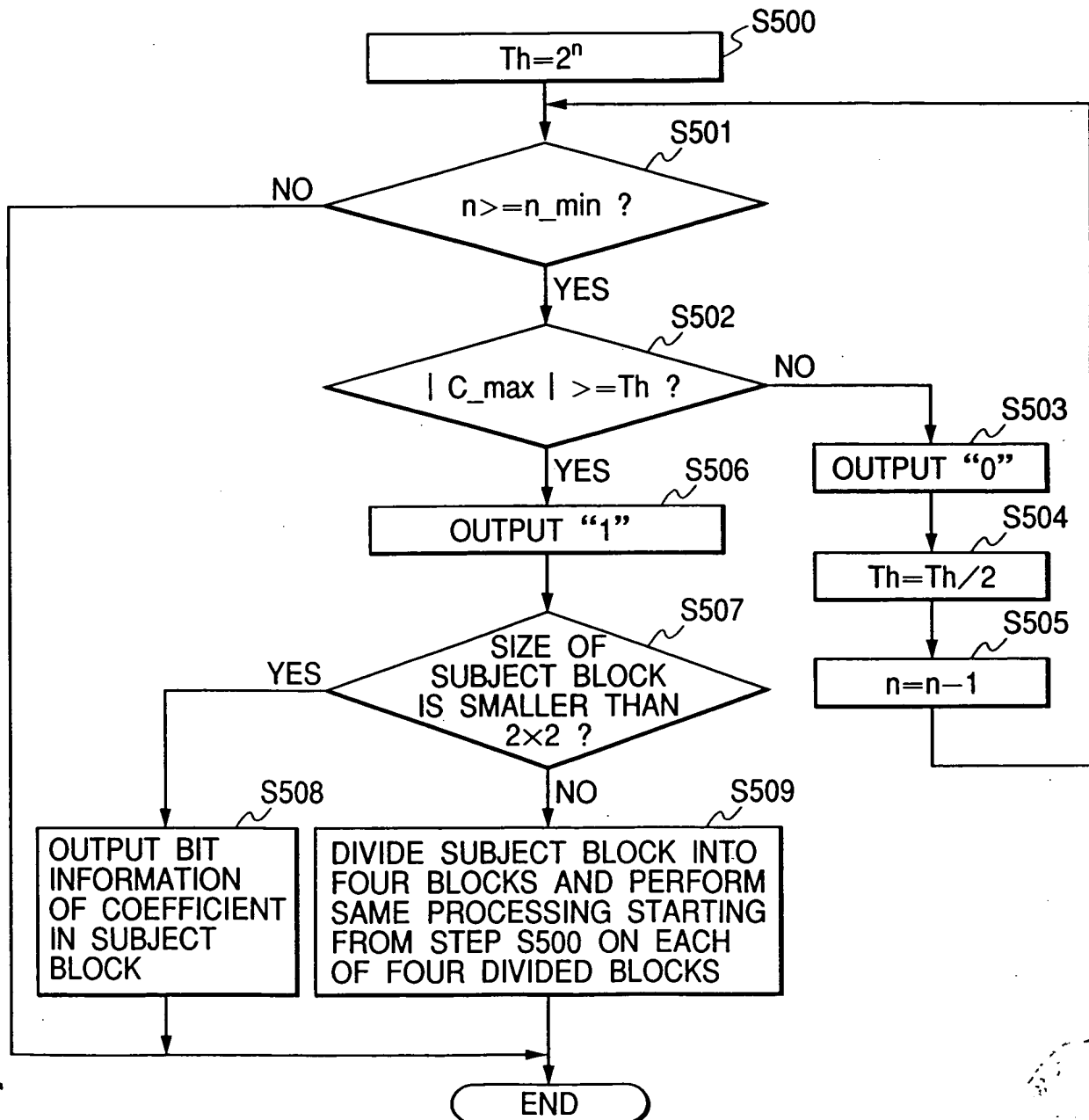
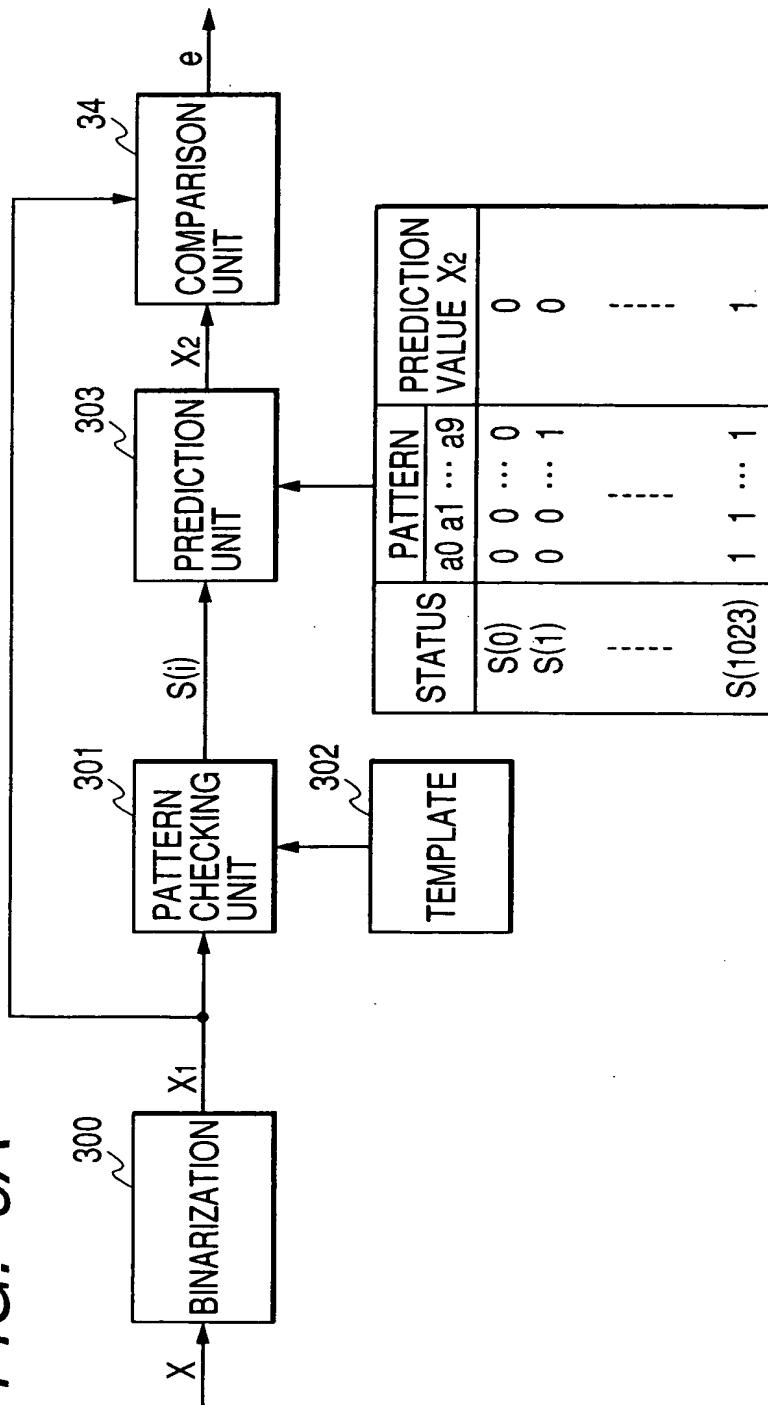


FIG. 6A



|    |    |    |    |    |    |
|----|----|----|----|----|----|
| a0 | a1 | a2 | a3 | a4 | a5 |
| a6 | a7 | a8 | a9 | X1 |    |

FIG. 6B

FIG. 7A



HEADER INFORMATION

FIG. 7B



FIG. 7C

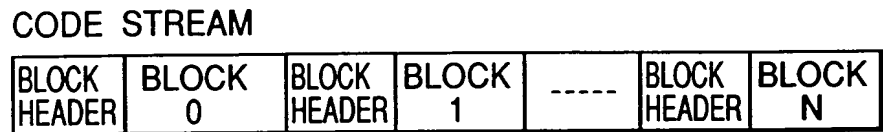


FIG. 7D

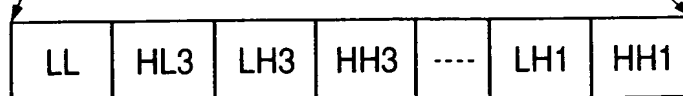


FIG. 7E



FIG. 7F

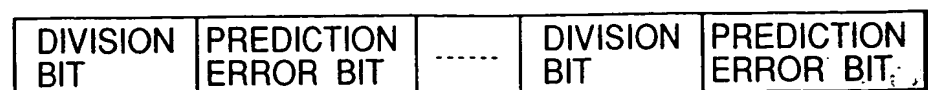


FIG. 8

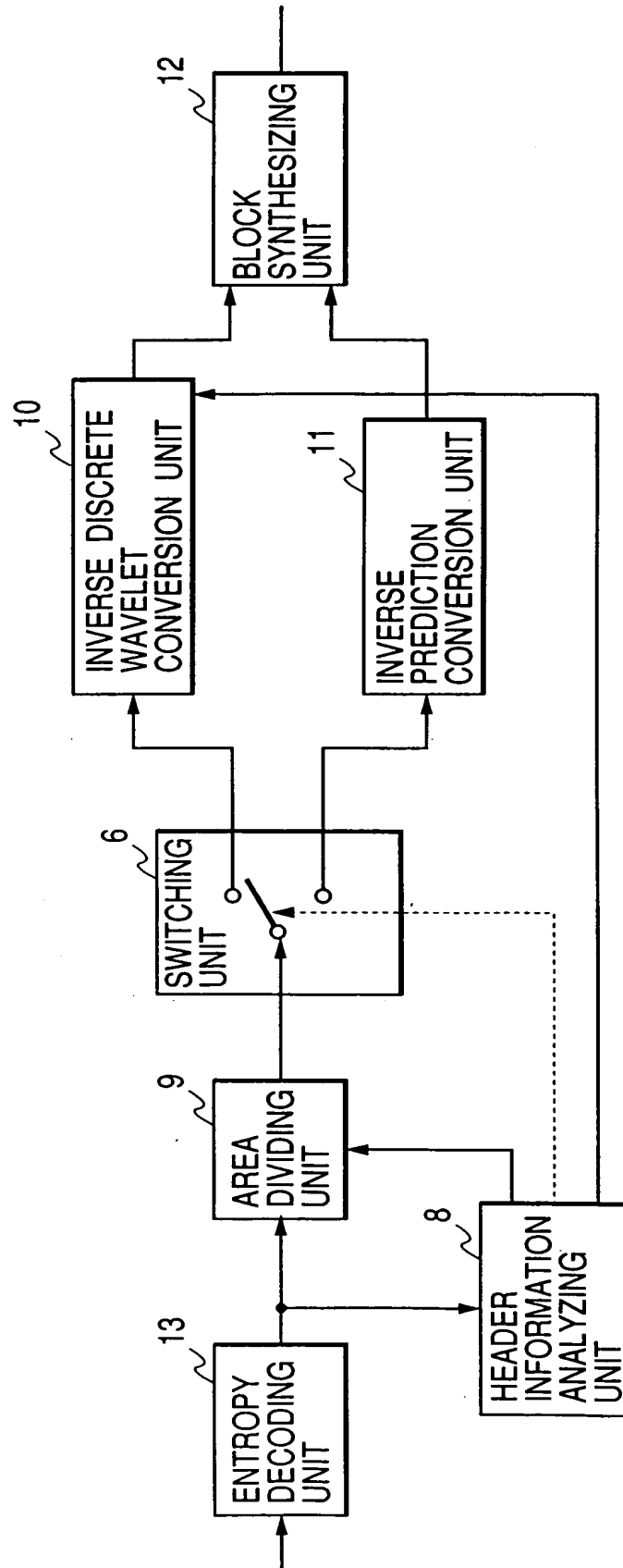




FIG. 9A      FIG. 9B      FIG. 9C

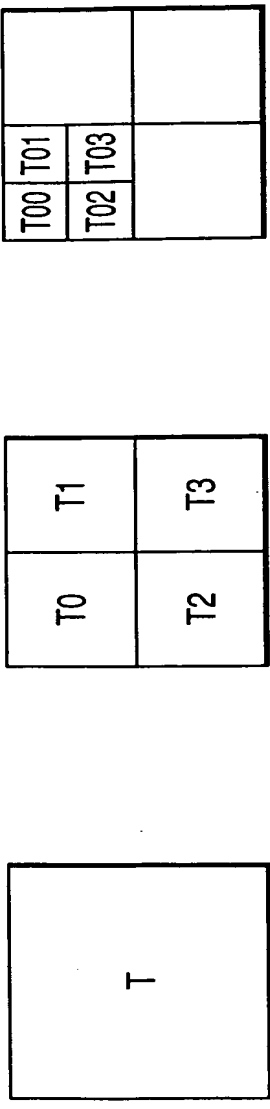


FIG. 9D      FIG. 9E

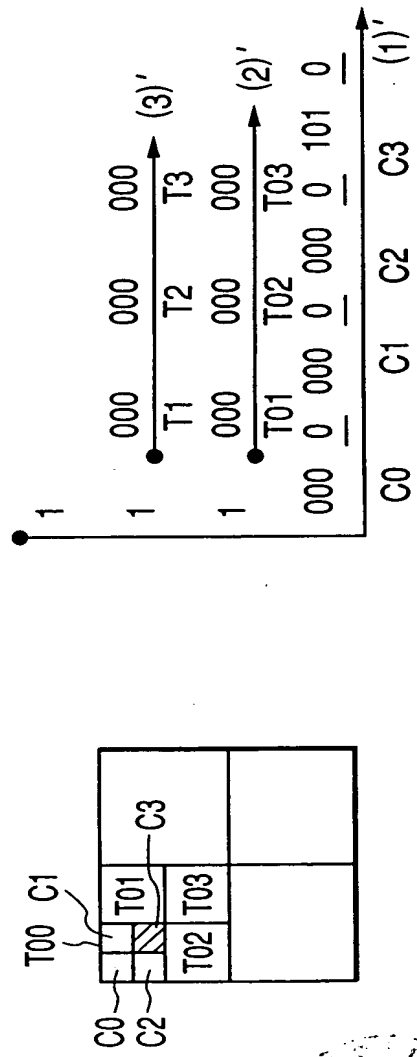


FIG. 9F

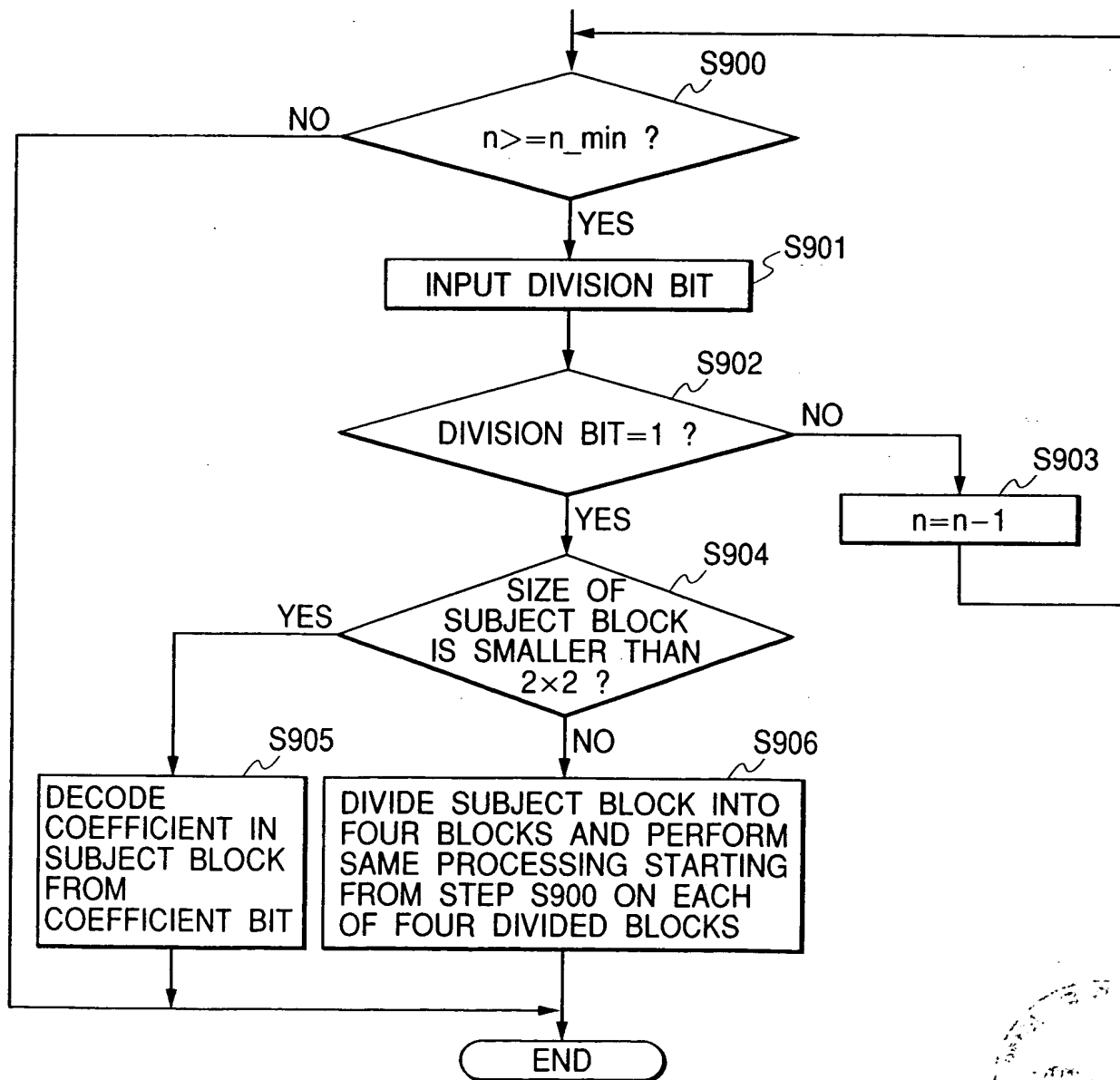


FIG. 10

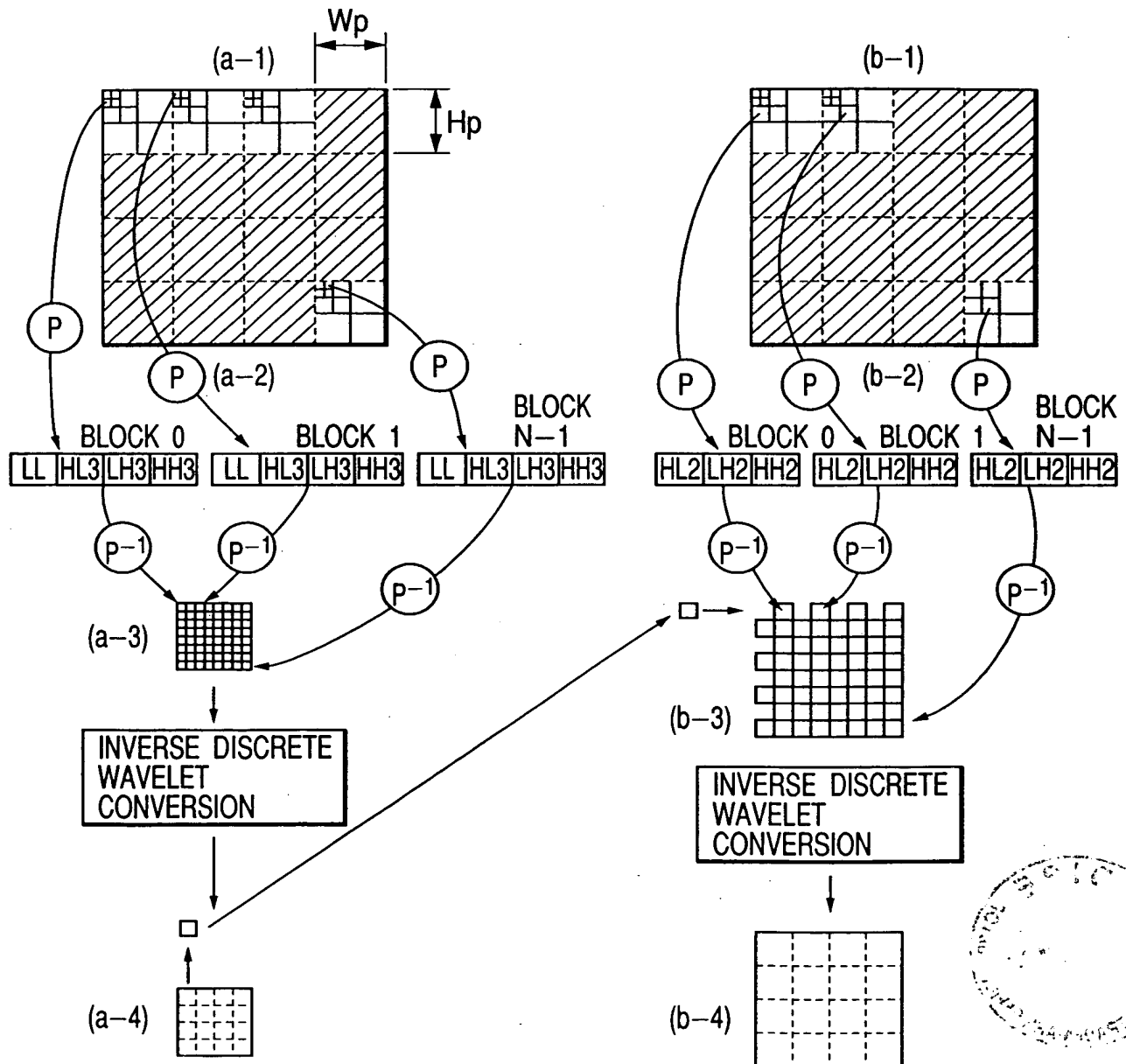


FIG. 11A

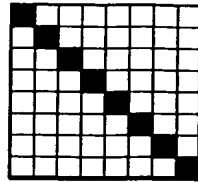


FIG. 11B

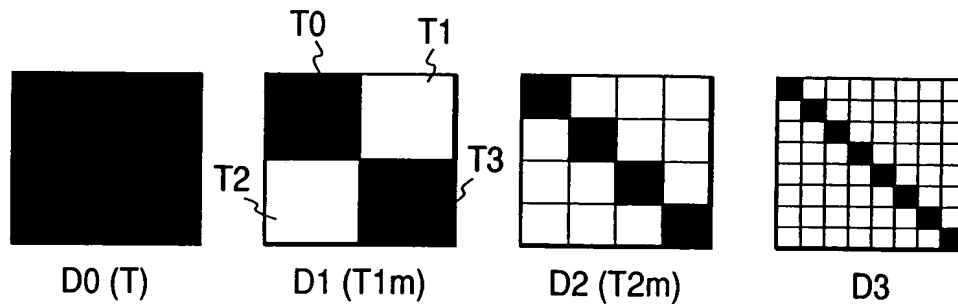
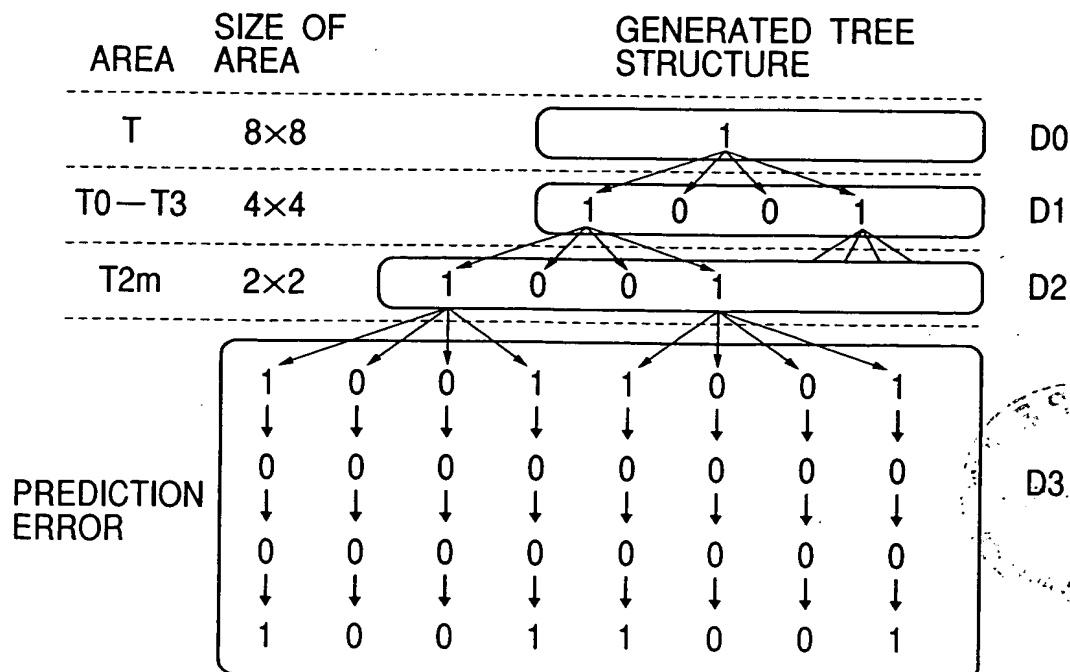


FIG. 11C



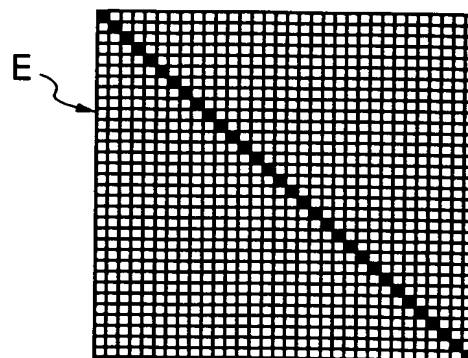
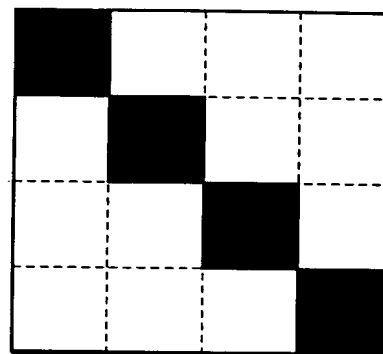
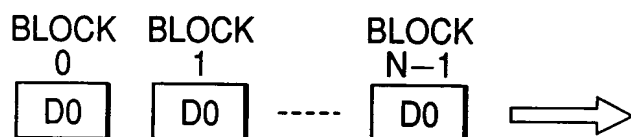
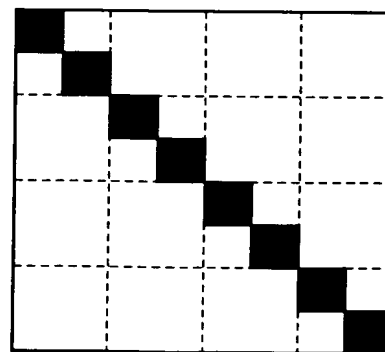
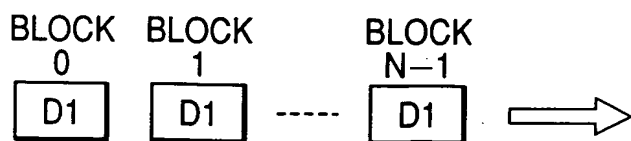
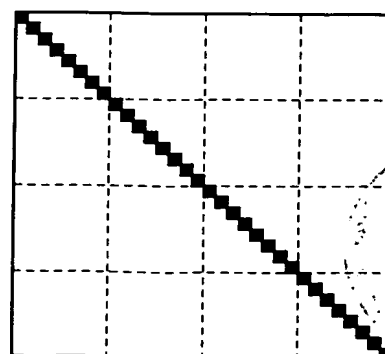
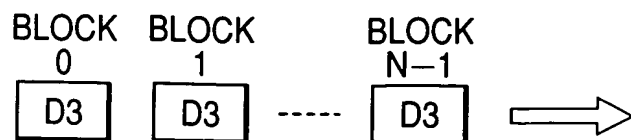
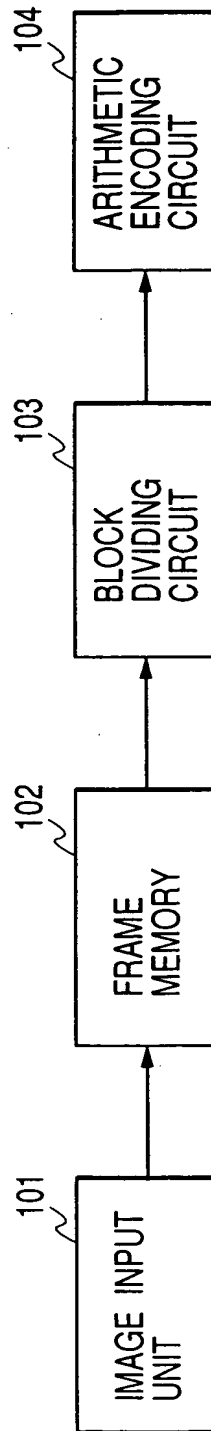
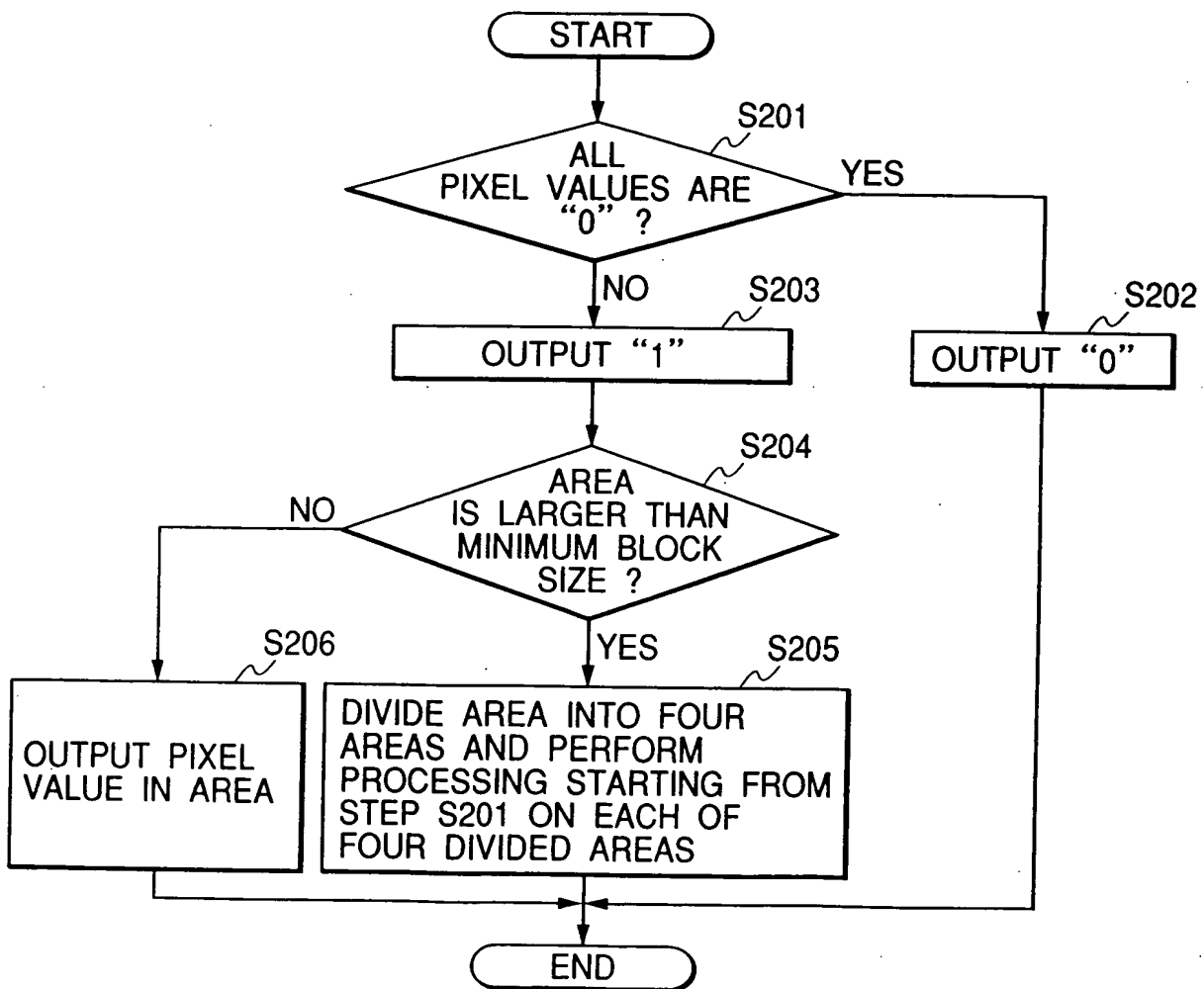
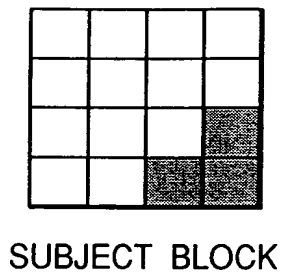
*FIG. 12A**FIG. 12B**FIG. 12C**FIG. 12D*

FIG. 13

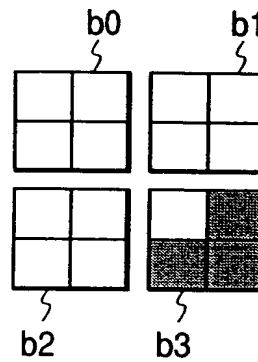


*FIG. 14*

*FIG. 15A*



*FIG. 15B*



*FIG. 16*

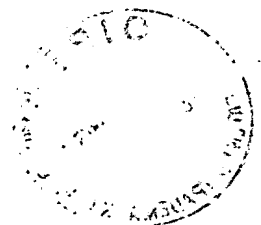
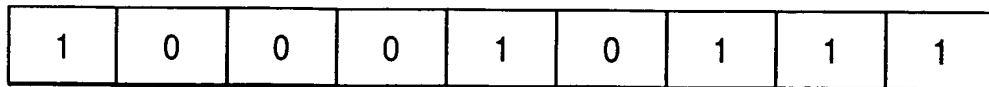




FIG. 17

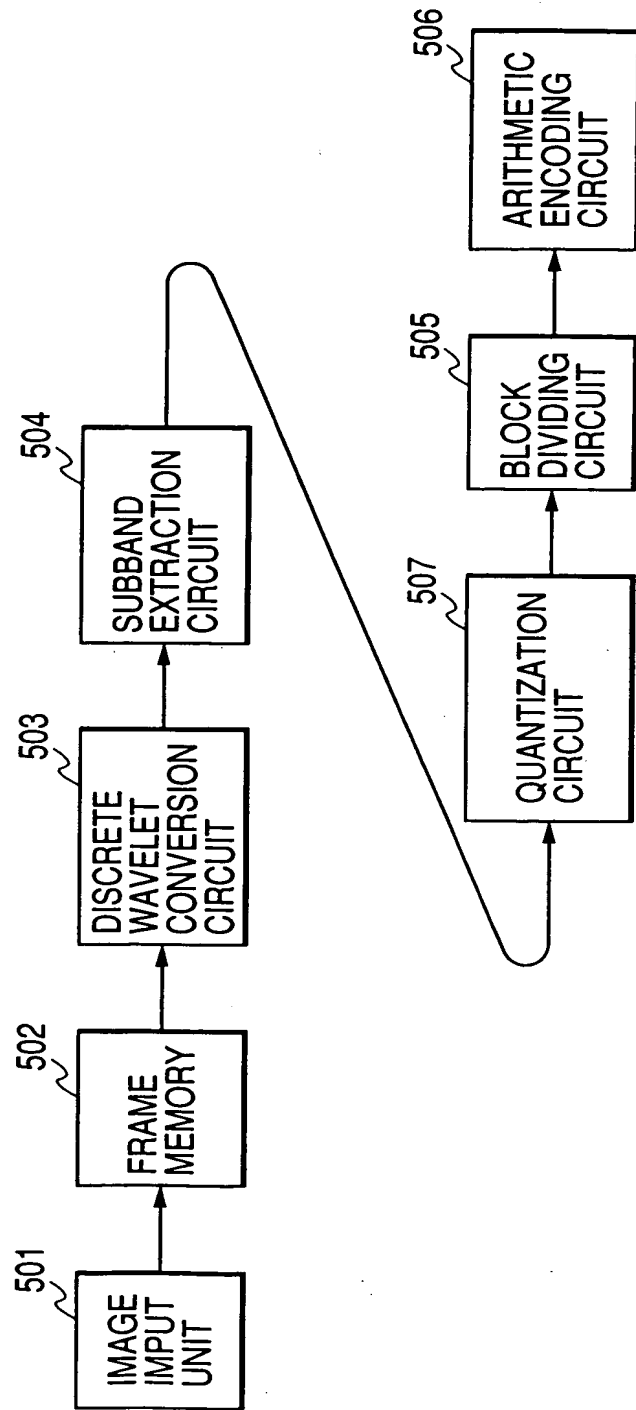


FIG. 18A

|    |    |
|----|----|
| LL | HL |
| LH | HH |

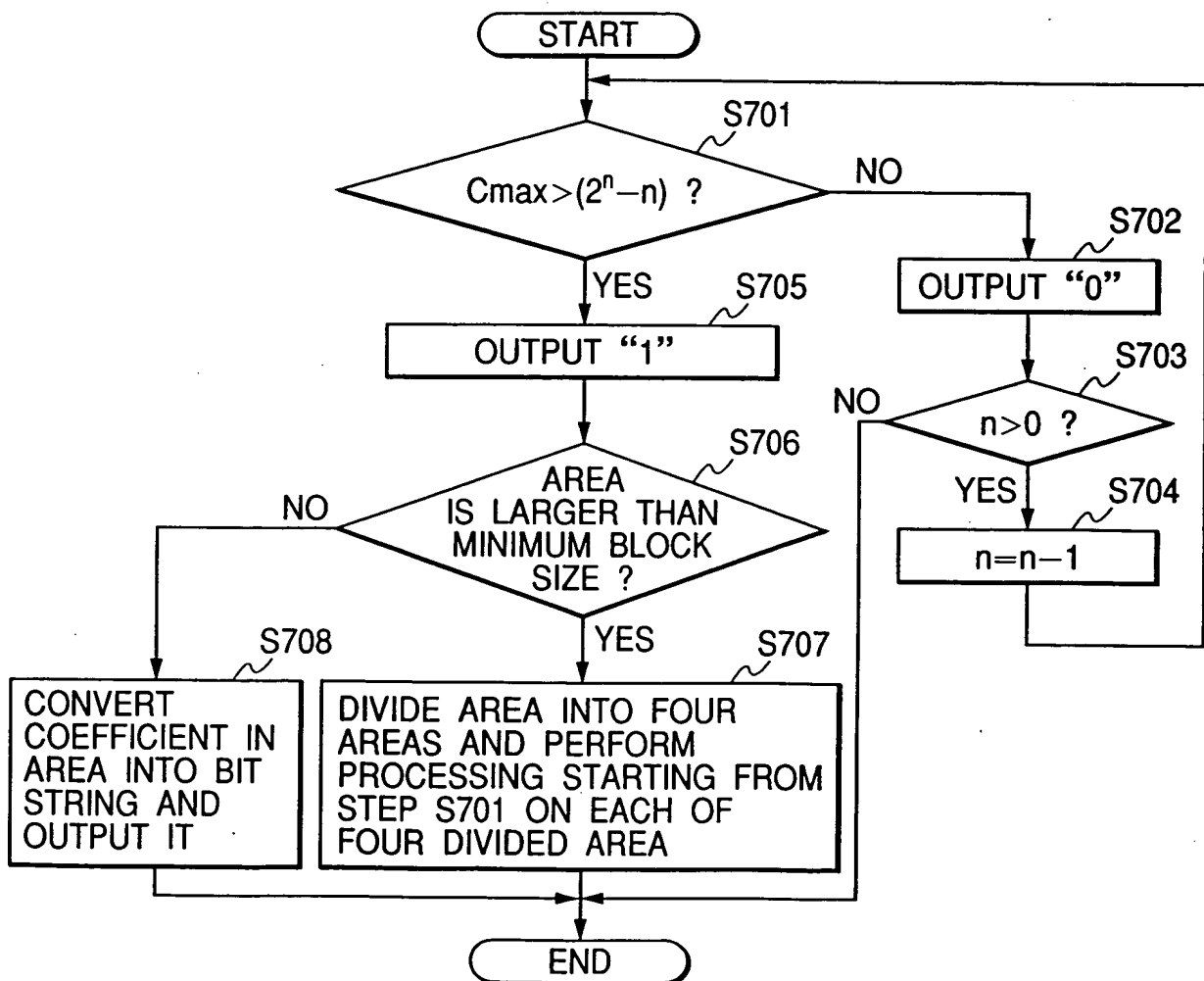
FIG. 18B

|    |    |
|----|----|
| LL | HL |
| LH | HH |

FIG. 18C

|     |     |     |     |
|-----|-----|-----|-----|
| LL4 | HL4 | HL2 | HL1 |
| LH4 | HL3 | HL2 | HL1 |
| HH4 | HL3 | HL2 | HL1 |
| LH3 | HL3 | HL2 | HL1 |

FIG. 19



*FIG. 20*

